

Seat
No.

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मध - 039

Microprocessor Techniques (Old) (1040)

P. Pages : 2

Time : Three Hours

Max. Marks : 100

Instructions to Candidates :

1. Do not write anything on question paper except Seat No.
2. Answersheet should be written with blue ink only. Graph or diagram should be drawn with the same pen being used for writing paper or black HB pencil.
3. Students should note, no supplement will be provided.
4. Attempt **any one** question from each unit.
5. Assume suitable data, if necessary.
6. Non Programmable Calculator is allowed.

UNIT - I

1. a) Interface two 2.k RAM to 8085. Explain its organization. 10
b) Draw and explain the structure of static RAM. Interface it to 8085. 10

OR

2. p) Explain in detail refreshing logic for dynamic RAM with suitable diagram. 10
q) Classify the semiconductor memories with respect to their structure. 10

UNIT - II

3. a) Explain basic machine cycles of 8085 microprocessor. 10
b) Write a program for addition of two two-byte numbers using 8085 assembly language. 10

OR

4. p) Explain the instructions. 10
(i) LXI (ii) MOV (iii) MVI
(iv) LDA (v) STAX.

- q) Explain the function of following pins of 8085. 10
- i) Clock out ii) Reset in iii) Reset out
- iv) S0 v) S1

UNIT - III

5. a) Explain in details interrupt handling and interrupt service routine. 10
- b) Explain IO mapped IO using suitable diagram. 10

OR

6. p) Draw timing diagram for wait cycle. 10
- q) What are the different Pseudo instructions used for 8085. 10

UNIT - IV

7. a) Interface 8279 with 8085 with different IOs. 10
- b) Explain control modes of 8255 PPI. 10

OR

8. p) Explain different modes of 8253 IC. 10
- q) Interface 8255 with 8085 with 4 x 4 matrix key board. 10

UNIT - V

9. a) Explain the Significance of RS232 c interface in details. 10
- b) Draw microprocessor system containing memories, IO devices and peripherals. Explain its organization. 10

OR

10. p) Draw the architecture of 6800. 10
- q) Draw IEEE 488 bus standards with interface. 10
